



PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of

Attn: OIPE

Ichio YUDASAKA et al.

Application No.: 10/717,503

Docket No.: 117849

Filed: November 21, 2003

For: TRANSISTOR, INTEGRATED CIRCUIT, ELECTRO-OPTIC DEVICE,
ELECTRONIC INSTRUMENT AND METHOD OF MANUFACTURING A
TRANSISTOR

REQUEST FOR CORRECTION OF PALM RECORDS

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Attached is a photocopy of the original filing receipt on which errors have been corrected in red. These errors are being brought to the attention of the Patent and Trademark Office so that it may correct its records. A Supplemental Application Data Sheet is attached.

Respectfully submitted,

James A. Oliff
Registration No. 27,075

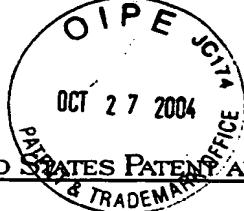
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Date: October 27, 2004

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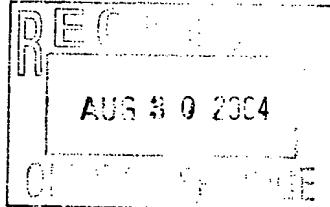


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APPL NO.	FILING OR 371 (c) DATE	ART UNIT	FIL FEE REC'D	ATTY.DOCKET NO	DRAWINGS	TOT CLMS	IND CLMS
10/717,503	11/21/2003	2811	770	117849	10	15	3

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CONFIRMATION NO. 7617

FILING RECEIPT



OC000000013657411

Date Mailed: 08/27/2004

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Applicant(s)

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Domestic Priority data as claimed by applicant

Foreign Applications

JAPAN 2002-351112 12/03/2002

If Required, Foreign Filing License Granted: 08/26/2004

Projected Publication Date: To Be Determined - pending completion of Missing Parts

Non-Publication Request: No

Early Publication Request: No

Title

Transistor, integrated circuit, electro-optic device, electronic instrument and method of

manufacturing a transistor

Preliminary Class

257

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